



Changelog:
 Rev. 1.0: Initial Release
 Rev. 1.1: Separated "clean" 5V Line from "dirty" VCC line. (Never released)
 Rev. 1.2: Inserted caps on OpAmp Inputs to compensate OPamp DC Offsets.
 +Added Reverse Polarity protection (Diode + Polyfuse)

DL2MAN & PE1NNZ

Sheet: /
 File: truSDX_Main_1.2.sch

Title: (tr)uSDX Main Board with IO Board

Size: A3	Date: 2023-04-02	Rev: 1.2
KiCad E.D.A. kicad (5.1.10)-1		Id: 1/1